



TERMINAL DISCLAIMER TO OBViate A DOUBLE PATENTING
REJECTION OVER A PRIOR PATENT

Docket Number (Optional)
42P9347C

In re the Application of: Srinivasan T. Rajappa, et al.

Application No.: 10/813,145

Filed: March 30, 2004

For: Method And Apparatus For A Low Latency Source- Synchronous Address Receiver For A Host System Bus In A Memory Controller

The owner*, Intel Corporation of 100 percent interest in the instant application hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the instant application, which would extend beyond the expiration date of the full statutory term defined in 35 U.S.C. 154 and 173, as presently shortened by any terminal disclaimer, of prior Patent No. 6,748,513. The owner hereby agrees that any patent so granted on the instant application shall be enforceable only for and during such period that it and the prior patent are commonly owned. This agreement runs with any patent granted on the instant application and is binding upon the grantee, its successors or assigns.

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2. The undersigned is an attorney of record.



Signature

01-31-05

Date

Joseph Lutz, Reg. No. 43,765
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Terminal disclaimer fee under 37 CFR 1.20(d) included.

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